

I CLAIM

1. A micro-electrical-mechanical-switch, MEMS, monolithic semiconductor device comprising

5 a semiconductor wafer, a first dielectric layer formed on the semiconductor wafer, and
a second semiconductor layer formed on the first layer;

10 a first latching movable shuttle formed in the second layer and having the first layer removed under the first shuttle, the first shuttle being moved in a first direction relative to the wafer in response to a predetermined acceleration of the MEMS device in a
direction opposite to the first direction so as to change an operating condition of the
MEMS device from a first switch state to an intermediate switch state;

15 a second latching moveable shuttle formed within the first shuttle, the second shuttle being moved in a second direction relative to the first shuttle in response to a thermally activated force so as to change the operating state of the MEMS device from the
intermediate switch state to a second switch state; and

wherein in the second switch state an opening in the second shuttle aligns with an opening in the wafer to enable an optical signal to pass through the aligned openings.

2. The MEMS device of claim 1 further comprising

means for preventing the movement of the second shuttle prior to the movement of the first shuttle.

3. The MEMS device of claim 1 further comprising

an electrical switch for providing an electrical switch connection when the MEMS device is in the intermediate state.

4. The MEMS device of claim 1 further comprising

an absorbing stop formed in the second layer for limiting the movement of the first shuttle in the first direction.

5. The MEMS device of claim 1 further comprising

a thermal activator responsive to an electrical signal for generating the thermally activated force.

6. The MEMS device of claim 5 wherein the thermal activator includes one or more arms having a push-rod mounted perpendicular to the midpoint of the one or more arms and where in response to the passage of the electrical signal through the one or more arms, the one or more arms expand and deflect causing the push-rod to generate
5 the thermally activated force.

7. The MEMS device of claim 1 wherein the second direction of movement is perpendicular to the first direction of movement.

8. The MEMS device of claim 1 wherein the semiconductor wafer is silicon, the first layer is silicon oxide and the second layer is silicon.

9. The MEMS device of claim 1 being formed by

A. first patterning and etching the movable elements in the second layer and stopping on the first dielectric layer,

- 5 B. patterning and etching a predetermined pattern in the semiconductor wafer from the bottom surface and stopping on the first dielectric layer,
- C. etching away the exposed regions of the first dielectric layer, and
- D. continuing this etch to allow undercutting sufficient to free the moveable elements from the semiconductor wafer.

10 A micro-electrical-mechanical-switch, MEMS, monolithic semiconductor device comprising

- a semiconductor wafer, a first electrically insulating layer formed on the semiconductor wafer, and a second semiconductor layer formed on the first layer;
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- a latching movable shuttle switch formed in the second layer and having the first layer removed under the shuttle switch, the shuttle switch being moved in a first direction relative to the wafer in response to a predetermined acceleration of the MEMS device
- 10 in a direction opposite to the first direction, thereby changing an operating state of the shuttle switch.

11. The MEMS device of claim 10 wherein the shuttle switch includes

an electrical switch for providing an electrical switch connection when the MEMS device has undergone the predetermined acceleration.

12. The MEMS device of claim 10 wherein

- the shuttle switch includes a first circular window formed therein which is vertically aligned but horizontally displaced from a second circular window formed in the wafer, and wherein in response to the MEMS device undergoing the predetermined
- 5 acceleration the shuttle switch is moved so that the first circular window is horizontally

moved in the first direction to be aligned over the second circular window, to thereby enable an optical signal to pass through the first and second circular windows.

13. The MEMS device of claim 10 further comprising

an absorbing stop formed in the second layer for limiting the movement of the shuttle switch in the first direction.

14. The MEMS device of claim 10 wherein the semiconductor wafer is silicon, the first layer is silicon oxide and the second layer is silicon.

15. A method of operating a MEMS monolithic semiconductor device comprising a semiconductor wafer, a first dielectric layer formed on the semiconductor wafer, and a second semiconductor layer formed on the first layer, the method comprising the steps of:

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latching a first movable shuttle formed in the second layer and having the first layer removed under the first movable shuttle, the first movable shuttle being moved in a first direction relative to the wafer in response to a predetermined acceleration of the MEMS device in a direction opposite to the first direction; the latched first movable shuttle changing a operating state of the MEMS switch from a first state to an intermediate state and

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latching a second moveable shuttle formed within the first shuttle, the second shuttle being moved in a second direction relative to the first shuttle in response to a thermally activated force so as to change the operating state of the MEMS switch device from the intermediate state to a second state.

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